

FIG. 1A
(PRIOR ART)

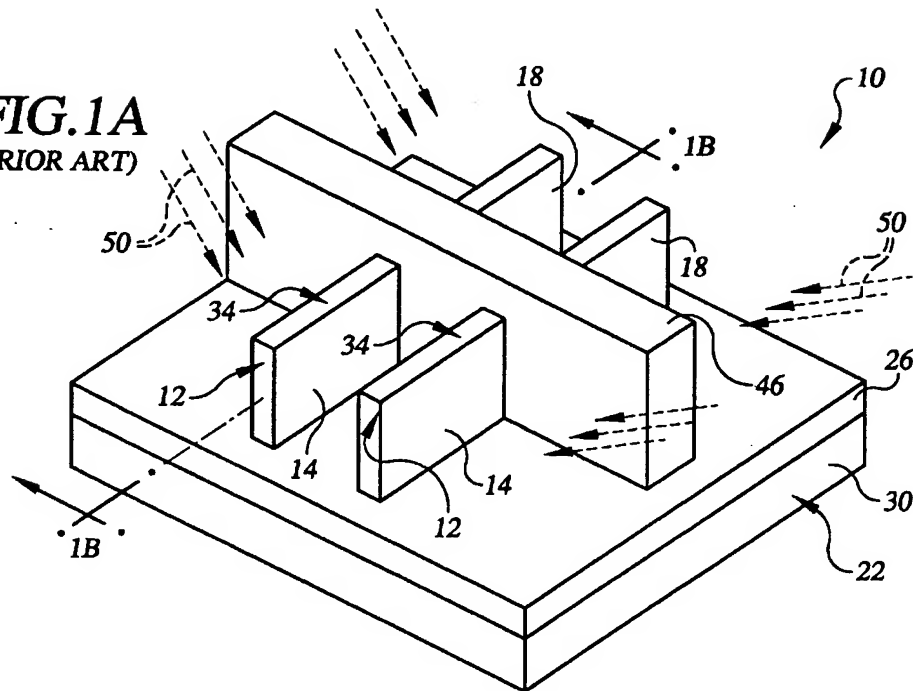
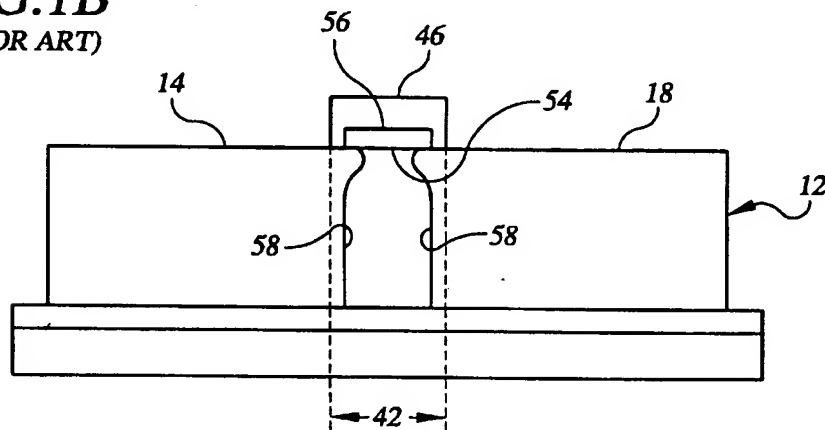


FIG. 1B
(PRIOR ART)



METHOD OF MAKING A *fin*FET HAVING SUPPRESSED
PARASITIC DEVICE CHARACTERISTICS
BUR920030005US1
NOWAK ET AL.
2/5

FIG.2

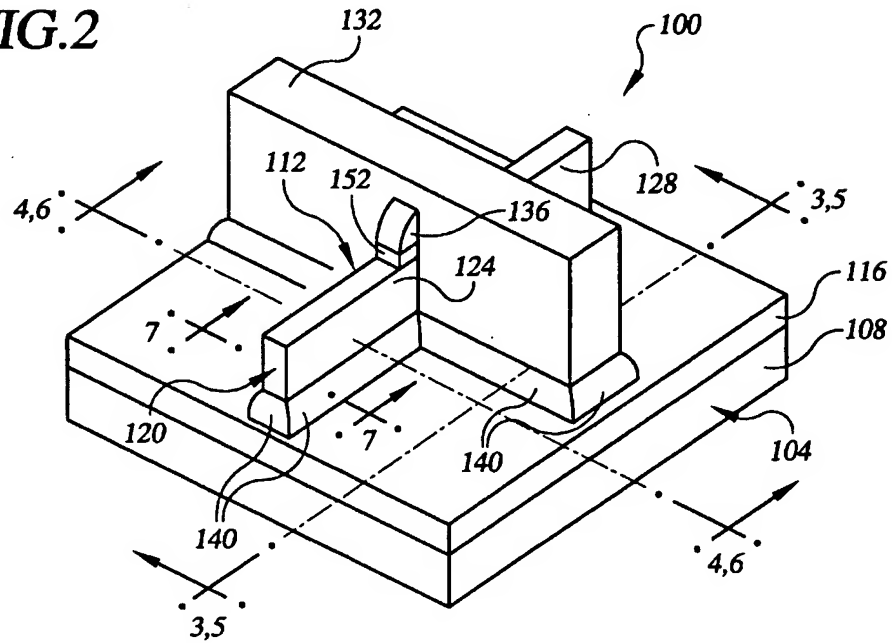
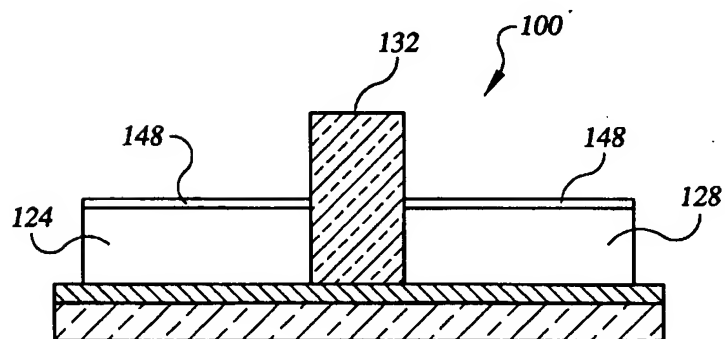


FIG.3



METHOD OF MAKING A *fin*FET HAVING SUPPRESSED
PARASITIC DEVICE CHARACTERISTICS
BUR920030005US1
NOWAK ET AL.
3/5

FIG.4

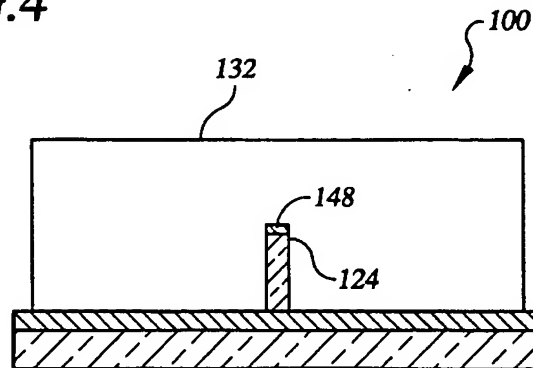
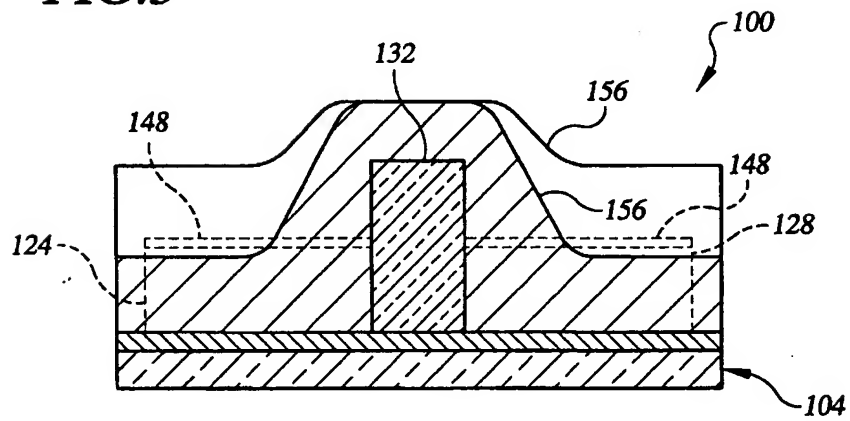


FIG.5



4/5

FIG.6

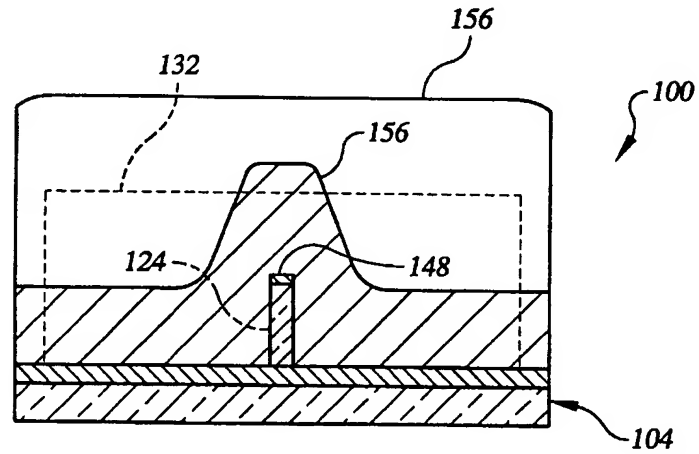


FIG. 7

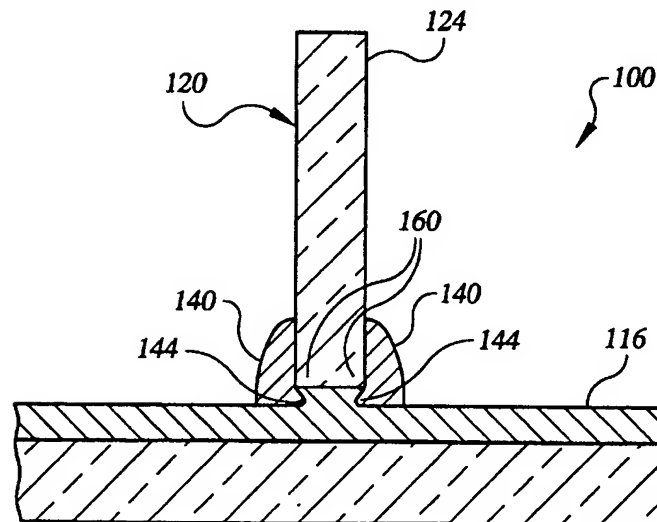


FIG.8

